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Electronic Version v1.1

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Title of
Invention

METHOD AND APPARATUS FOR CONNECTING LPC BUS AND SERIAL FLASH MEMORY

Application Number:

Date:

First Named Applicant:

Mr. Yao-Cheng Chiu

Confirmation Number:

Attorney Docket Number:

13693-US-PA

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Documents being submitted:	Files
us-assignment	13693-usassn.xml
55 to 5g	us-assignment.xsl
	us-assignment.dtd
	13693assignment01.tif
	13693assignment02.tif
us-request	13693-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	13693-usfees.xml
	us-fee-sheet.xsl
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application-body	13693usf.xml
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	Image1.tif
	Image10.tif
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	isoamsa.ent
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	mathml2.dtd
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mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent 13693usf-pdf-wrap.xml application-body-pdf-wrap 13693usf-abst.pdf abstract-pdf claims-pdf 13693usf-clms.pdf description-pdf 13693usf-desc.pdf drawings-pdf 13693usf-draw.pdf us-declaration 13693declaration01.tif 13693declaration02.tif us-declaration

Comments

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